

[Method and System for Injecting Virtual Flaw Signals into a Nondestructive Test System]

Abstract

The invention provides an electronic circuit means for injecting virtual flaw signals into the signal path between a NDT test instrument and an associated probe. This enables a system that is capable of generating virtual flaw signals to present virtual flaws to an NDT inspector while enabling the test probe to present actual flaws to the NDT inspector. An eddy current test (ECT) embodiment of the invention comprises a means for deriving a reference signal from an ECT instrument excitation signal, a means for modulating the gain and phase of the reference signal by commands from a control computer, and a means for summing the modulated signal with the ECT probe output signal for transmission to the ECT instrument.